



Is HTM doomed
without progress
guarantees?
Too many
paths?



what will be the
impact of the
Haswell bug?
is HTM just too
hard?



Is Hybrid TM
hopeless without
non-transactional
writes?



What new features do we need in HTM?
What do we ask to the hardware designers?



TM is a black box that encompasses several building blocks, like logging, failure-atomicity, conflict detection...

Should we expose the HTM basic building blocks to applications?



TM and non-volatile memory: what are the important research questions in this area?



What is most critical
in order to improve
Transaction Lock
Elision?